

Rth

Connect a current source with 1A and find the voltage  $V_t$

$$v_1 = -i_1 R_2$$

$$i_3 = -1$$

super mesh

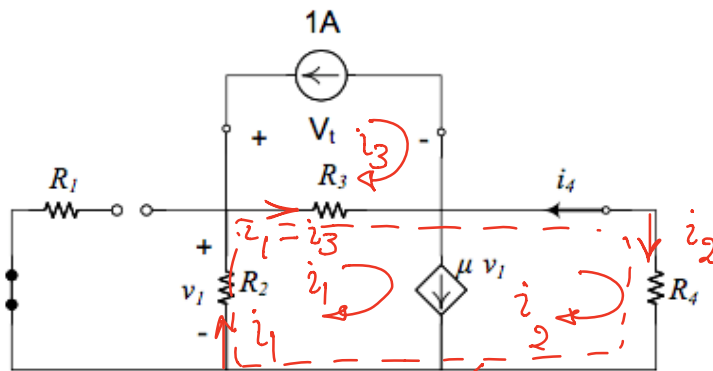
$$R_2 i_1 + R_3 (i_1 - i_3) + i_2 R_4 = 0$$

$$i_1 - i_2 = \mu v_1 = -\mu i_1 R_2$$

Solve for  $i_1$  &  $i_2$  &  $i_3$

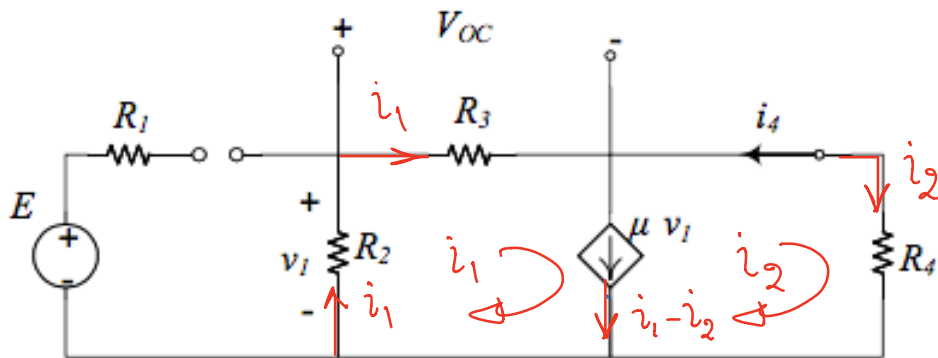
$$V_t = R_3 \times (i_1 - i_3)$$

$$R_{th} = V_t = R_3 (i_1 - i_3) = R_3 (i_1 + 1) = 7.4886$$



Supermesh

Voc



Supermesh  $i_1 R_2 + R_3 i_1 + i_2 R_4 = 0$  (1)

$$i_1 - i_2 = \mu v_1$$

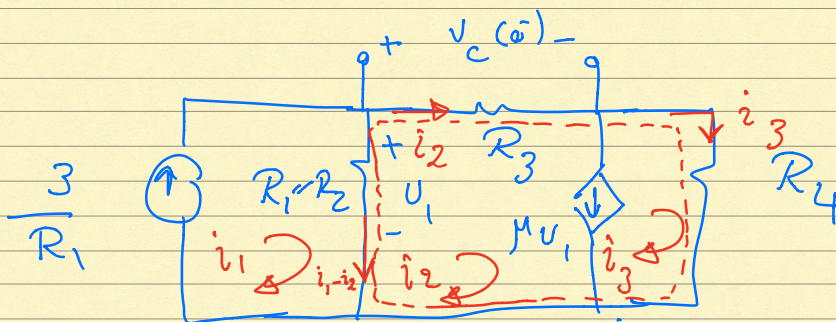
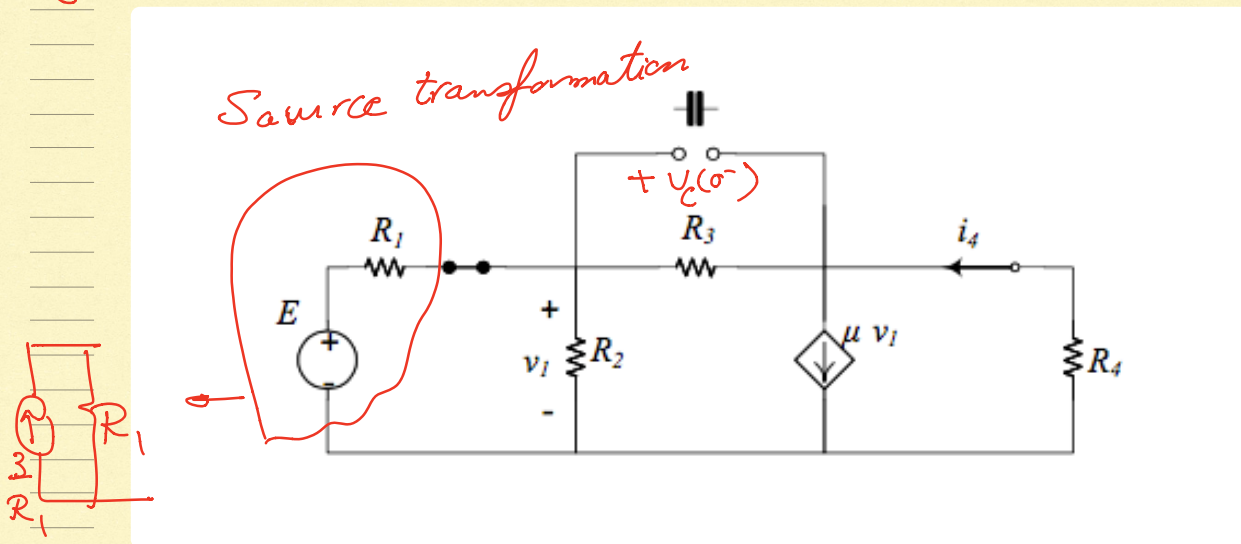
$$= -\mu i_1 R_2 \quad (2)$$

Solving eq (1) & (2), we find that

$$i_1 = i_2 = 0$$

$$V_{oc} = 0$$

$V_c(\infty)$



$$i_1 = \frac{3}{R_1},$$

← loop ①

$$-(i_1 - i_2)(R_1 \parallel R_2) + i_2 R_3 + i_3 R_4 = 0 \quad \leftarrow \text{supermesh}$$

$$i_2 - i_3 = \mu U_1$$

$$= \mu (i_1 - i_2) (R_1 \parallel R_2)$$

$\leftarrow$  current source

Solving (1), (2) & (3), we get  $i_1$ ,  $i_2$  &  $i_3$

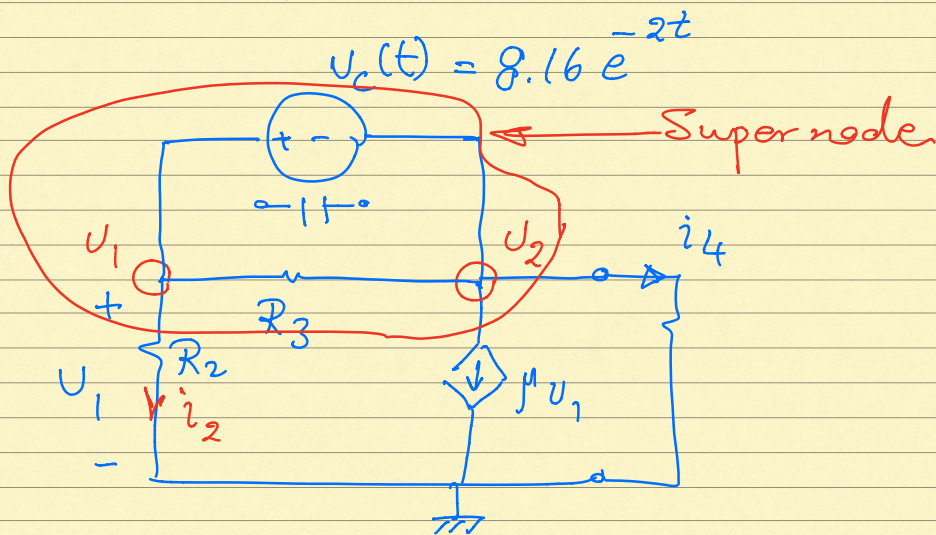
$$U_C(0^-) = i_2 R_3$$

$$= 8.16 \text{ V} = U_C(0^+)$$

$$U_C(t) = V_{OC} + [U_C(0^+) - V_{OC}] e^{-t/(R_T C)}$$

$$= 8.16 e^{-1.807t}$$

To find the current in the resistor, we make use of the now known voltage across the capacitor by representing the capacitor as a voltage source with voltage  $U_C(t)$



We can use the node voltage analysis method to find  $i_4(t)$ .

KCL @ Supernode

$$i_2 + i_4 + M u_1 = 0$$

$$\frac{u_1}{R_2} + \frac{u_2}{R_4} + M u_1 = 0 \quad (1)$$

from the Supernode

$$\begin{aligned} u_1 - u_2 &= u_c(t) \\ &= 8.16 e^{-1.806t} \end{aligned}$$

$$u_1 = u_2 + 8.16 e^{-1.806t} \quad (2)$$

Substituting from (2) into (1)

$$\left( \frac{1}{R_2} + M \right) \left( u_2 + 8.16 e^{-1.806t} \right) = \frac{u_2}{R_4}$$

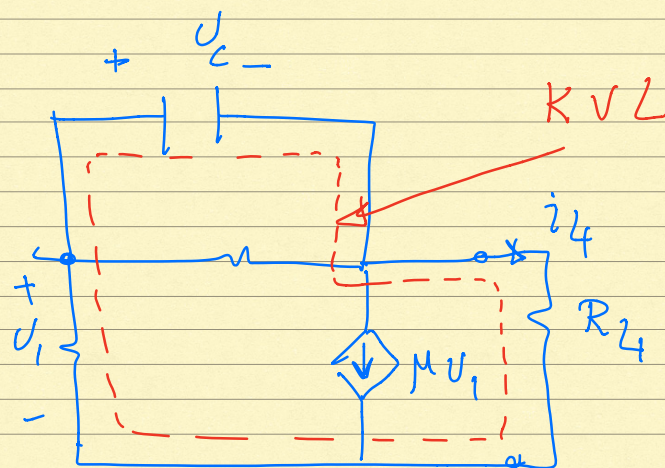
Find  $u_2$  from the above equation, then use it to find  $i_4$

$$i_4 = \frac{u_2}{R_4} = -\frac{102}{53} e^{-1.806t}$$

$$u_1 = \frac{612}{1325} e^{-1.806t}$$

To verify the solution apply KCL or KVL any where in the circuit and see that it is being satisfied.

For example, we can apply KVL around the loop shown below



KVL @ the loop

$$-V_1 + V_c + i_4 R_4 \text{ (must be)} = 0$$

Substituting for  $V_1$ ,  $V_c$  and  $i_4$ , we see that they satisfy the above KVL

Thus it satisfies KVL around the outer loop.