

Tutorial 4.

① Unsigned Numbers. (all positive).

Ex: $(12)_{10} = (1100)_2$.

Addition:

Properties: $\begin{array}{r} 0 \\ +0 \\ \hline 0 \end{array}$ $\begin{array}{r} 0 \\ +1 \\ \hline 1 \end{array}$ $\begin{array}{r} 1 \\ +0 \\ \hline 1 \end{array}$ $\begin{array}{r} 1 \\ +1 \\ \hline 0 \end{array}$; 1 carry out.

For unsigned numbers if • carry out = 1 \Rightarrow overflow occurs
• carry out = 0 \Rightarrow NO overflow.

Ex: $\begin{array}{r} 01001 \\ +0011 \\ \hline 1100 \end{array}$; carry out = 0.
No overflow.

} $\begin{array}{r} (4)_{10} \\ + (3)_{10} \\ \hline (12)_{10} \end{array}$

$\begin{array}{r} 1101 \\ +0110 \\ \hline 0011 \end{array}$; carry out = 1
overflow

- if it's specify the # of bits used, when (Carry out = overflow occurs \Rightarrow result is wrong.
- when no # of bits is specified, then the carry out must be part of the result.

Subtraction:

$\begin{array}{r} 01111 \\ -00111 \\ \hline 01010 \end{array}$ $\begin{array}{r} (17)_{10} \\ - (7)_{10} \\ \hline (10)_{10} \end{array}$

- when you borrow from one the next one has TWO "1".

Multiplication

$$\begin{array}{r}
 1110 \quad (14)_{10} \text{ \# same as normal} \\
 \times 0110 \quad (6)_{10} \text{ multiplication \#} \\
 \hline
 0000000 \\
 0011100 \\
 0111000 \\
 0000000 \\
 \hline
 1010100 \quad (84)_{10}
 \end{array}$$

Division

$$\begin{array}{r}
 1001 \overline{) 11011} \\
 \underline{1001} \\
 1000 \\
 \underline{1001} \\
 0100 \\
 \underline{1001} \\
 0000
 \end{array}$$

divide each bit by the divisor until you can divide after that normal division

- first try. 1 divided by 1001 = 0 then next.
- 11 divided by 1001 = 0.
- next; 110 by 1001 = 0
- Finally 1101 divide by 1001 = 1.

② Signed Numbers (2.1 Signed Magnitude representation)

$$(+7)_{10} \Rightarrow 0111$$

$$(-7)_{10} \Rightarrow 1111$$

0 = positive
1 = negative.

for Addition, example: add the magnitudes and use the sign of the bigger number.

2.2. 2's complement.

0011011		0011011
1100100	Invert (1's complement)	1100100
0000001	Add 1	0000001
1100001	→ 2's complement.	1100101

To convert to 2's complement directly.

• look at the last significant value.

if its 0 move to next

if its 1, take 1 and invert the rest.

ex: • 0011011 ← last significant is 1

↓ invert the rest

1100101

• 000100100010000 ← 0's

↓

11101101110000

→ 1, invert rest

Also,

In addition

carry out.

$$\begin{array}{r}
 a_{n-1} \dots a_1 a_0 \\
 + \quad b_{n-1} \dots b_1 b_0 \\
 \hline
 c_{n-1} \dots c_1 c_0
 \end{array}$$

$$V = C_n + C_{n-1}$$

if $V = 0 \Rightarrow$ No overflow

$V = 1 \Rightarrow$ ~~overflow~~
overflow

Exercise:

a) 26, -37, -123 as signed 10 bit binary numbers.

i) signed magnitude.

$(26)_{10} = (0000011010)_2$

$(-37)_{10} = (1000100101)_2$

$(-123)_{10} = (1001111011)_2$

↓ sign. ↓ magnitude.

ii) 2's complement.

$(26)_{10} = 0000011010$. ← Same magnitude, and now the sign is part of the number. (That's why tutor didn't box it).

$(-37)_{10}$ • keep the sign and convert only the magnitude.

$(-37)_{10} = 1111011011$ (1's complement)

$(-123)_{10} = 1110000101$ (1's complement)

• In 2's complement, positive numbers, results in same magnitude •

a2) 6 bits numbers in 2's complement.

a) 010110 (22)₁₀
+ 001001 (9)₁₀

b) 011001 (25)₁₀
+ 010000 (16)₁₀

Carry out ← 011111 (31)₁₀
V = 0

101001 (41)₁₀
V = 0 + 1 = 1

No overflow.

overflow

• overflow doesn't mean answer is wrong.

BUT

wrong answer means overflow •

in normal base: $\frac{-0}{-1} = 0$ (5)

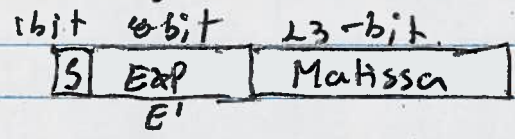
c)
$$\begin{array}{r} 010110 \quad (22)_{10} \\ - 011111 \quad (31)_{10} \\ \hline 110111 \quad (-9)_{10} \end{array} \quad \begin{array}{r} 00 \\ - 01 \\ \hline (11) \end{array} \quad \begin{array}{l} -1 \text{ to } 2\text{'s complement} \\ = 11 \end{array}$$

to check the answer, since its negative, we have to check its 2's complement, if its 9, then the result is correct.

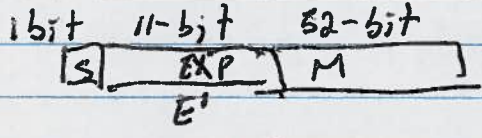
110111
 $001001 \rightarrow 9$; then 110111 is correct.

Floating Point Numbers Representation

IEEE 754 single precision. (32-bit).



IEEE 754 double precision. (64-bit).

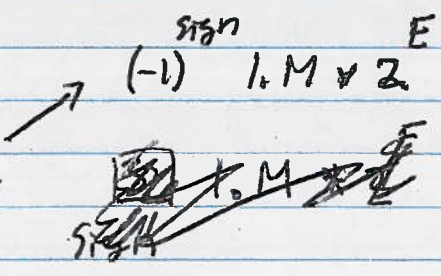


$E' = E + 127$ ← single precision.
 bias (excess).

to compute bias.

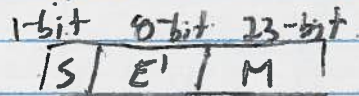
$$\begin{aligned} \text{bias} &= \frac{1}{2}(N-1) - 1 \quad ; \quad n = \# \text{ of bits for exponents.} \\ &= \frac{1}{2}(2^b - 1) - 1 \\ &= 2^{b-1} - 1 \\ &= 127. \end{aligned}$$

And the representation is:



6

Ex: express $(+7.5)_{10}$ in IEEE single precision.



$$\begin{aligned} \text{bias} &= 2^{N-1} - 1 \\ &= 2^{8-1} - 1 \\ &= 2^7 - 1 \\ &= 127. \end{aligned}$$

First convert to binary.

$$(7.5)_{10} = (111.1)_2$$

$2^2 \cdot 2^1$

and $111.1 = 1.111 \cdot 2^2$

sign

$$(-1) \cdot 1.111 \cdot 2^2$$

$$(-1)^0 \cdot 1.111 \cdot 2^2 \rightarrow E$$

M

$$E' = E + 127 = 2 + 127 = 129. \Rightarrow (10000001)_2$$

Answer =

$$\begin{array}{c} \underline{01000000111000000000000000000000} \\ \text{S} \quad \text{E}' \quad \text{M} \quad \text{20 0's} \end{array}$$

$$= 01000000111000000000000000000000$$